Matrix

Item	See Model	Book
Deck Exploded View	VXA 1105	6
Deck Alignment	VXA 1105	6
Deck Adjustments	VXA 1105	6

Electrical Adjustments

3. ADJUSTMENT PROCEDURE

Read and perform these adjustments when repairing the circuits or replacing electrical parts or PCB assemblies.

CAUTION
When replacing IC's or transistors, use only specified silicon grease (YG6260M).

(To prevent the damage to IC's and transistors.)

3-1: PG SHIFTER

CONDITIONS
MODE-PLAYBACK
Input Signal-Alignment Tape (JG001C)

INSTRUCTIONS
1. Playback the alignment tape. (JG001C)
2. Press both Tracking AUTO key on the Customer remote control and the PLAY button on the set simultaneously until the indicator ATR disappear's. If the indicator ATR disappear's, the adjustment is finished. (If the ATR indicator is still illuminated)

1. Connect CH-1 on the oscilloscope to TP4001 and CH-2 to Pin 19 of J4501.
2. Press the PG AUTO key. (JG155)
3. While the ATR indicator is flashing, press the PG MANUAL key. (JG155)
4. Adjust the Tracking +/- key until waveform of the oscilloscope measures 6.5 ± 0.5(H) at both leading and trailing edges. (Refer to Fig. 3-1-A, B)
5. Press the Tracking Auto key.

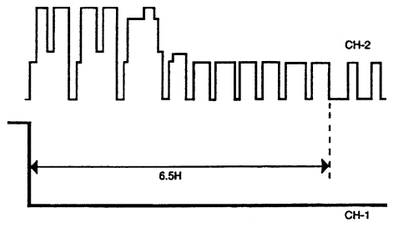


Fig. 3-1-A

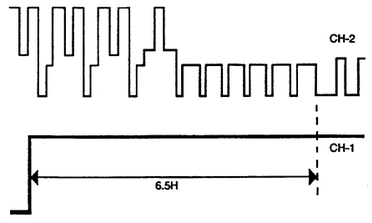
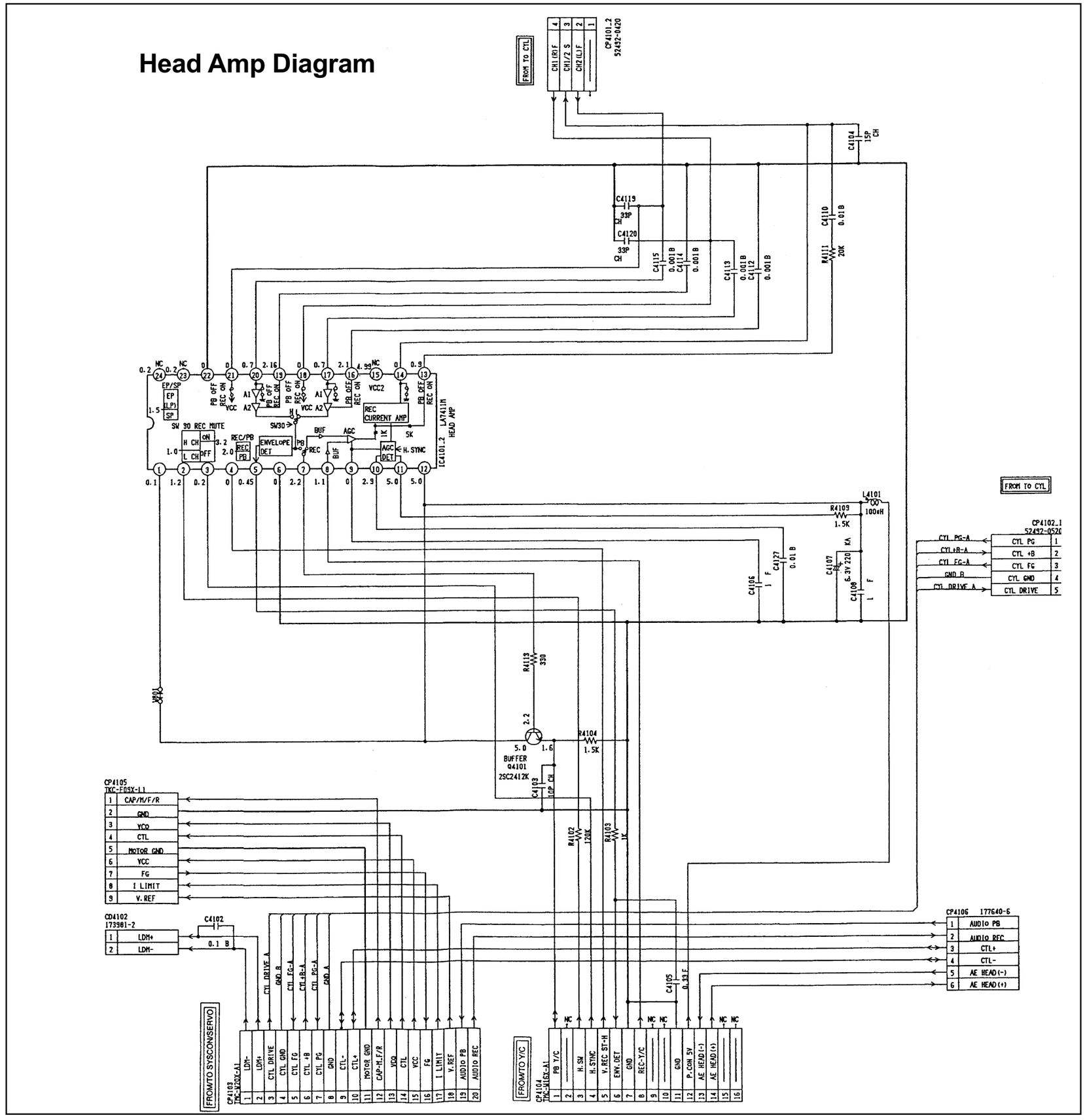
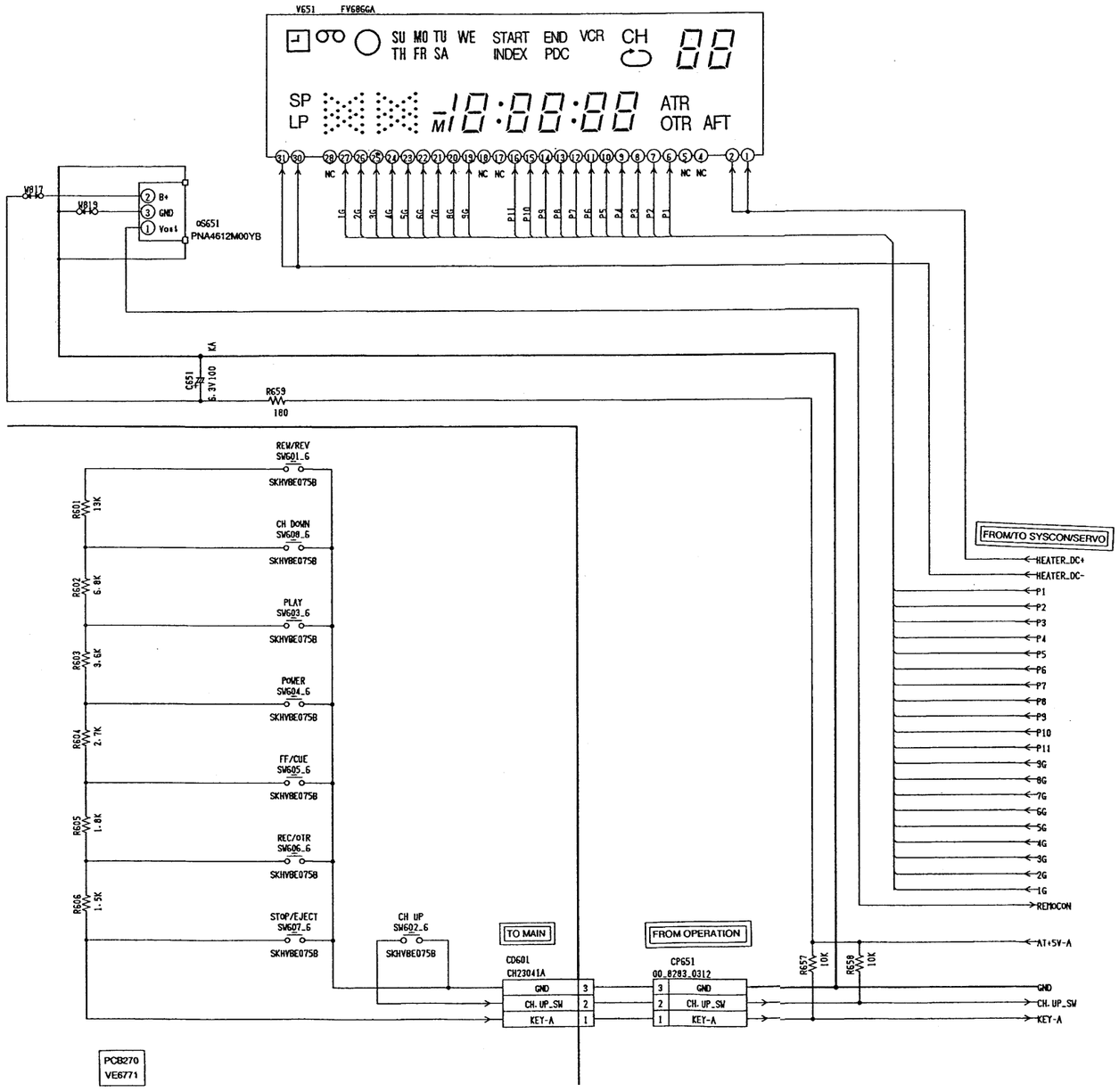


Fig. 3-1 -B

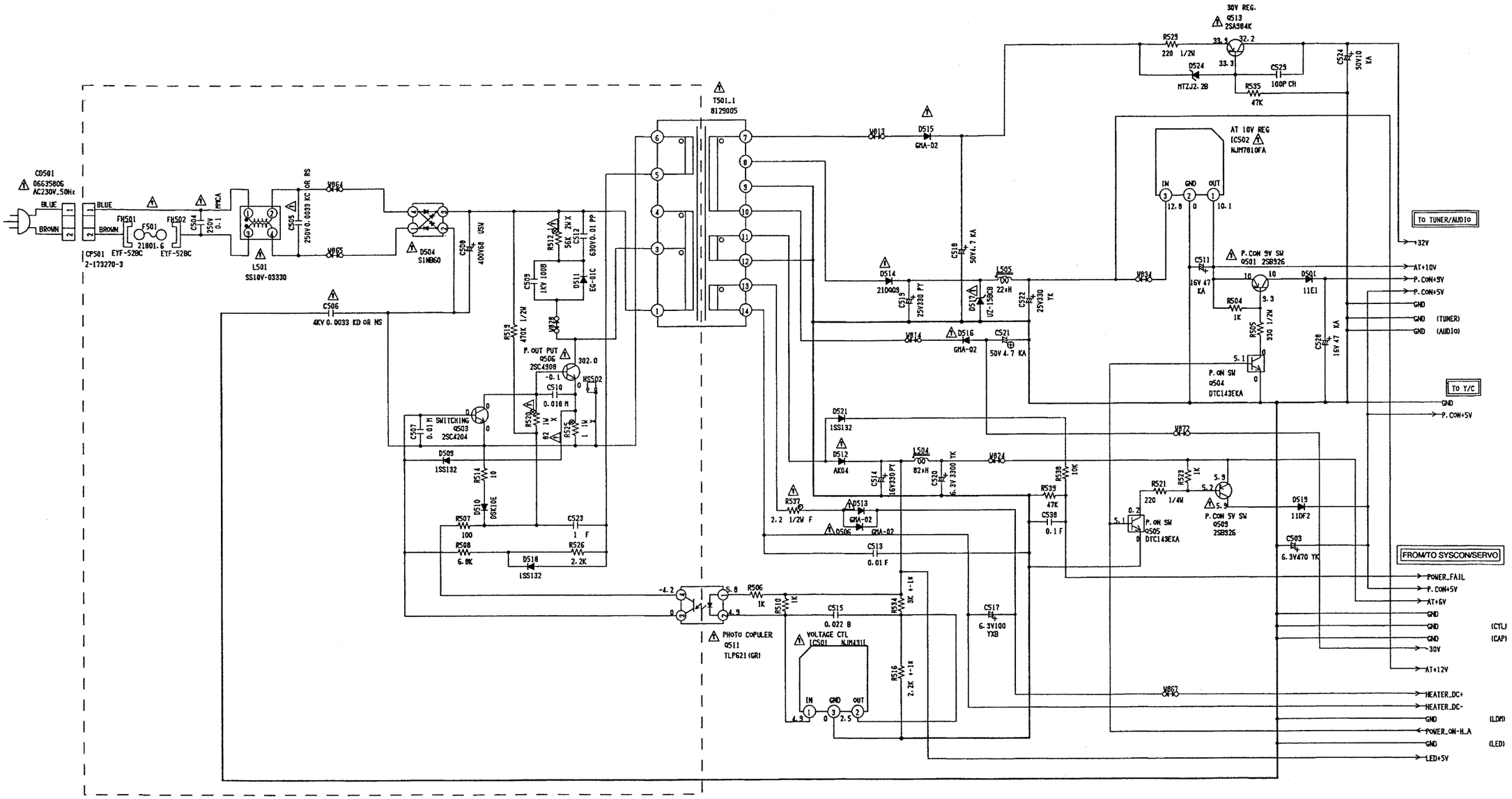
Head Amp Diagram



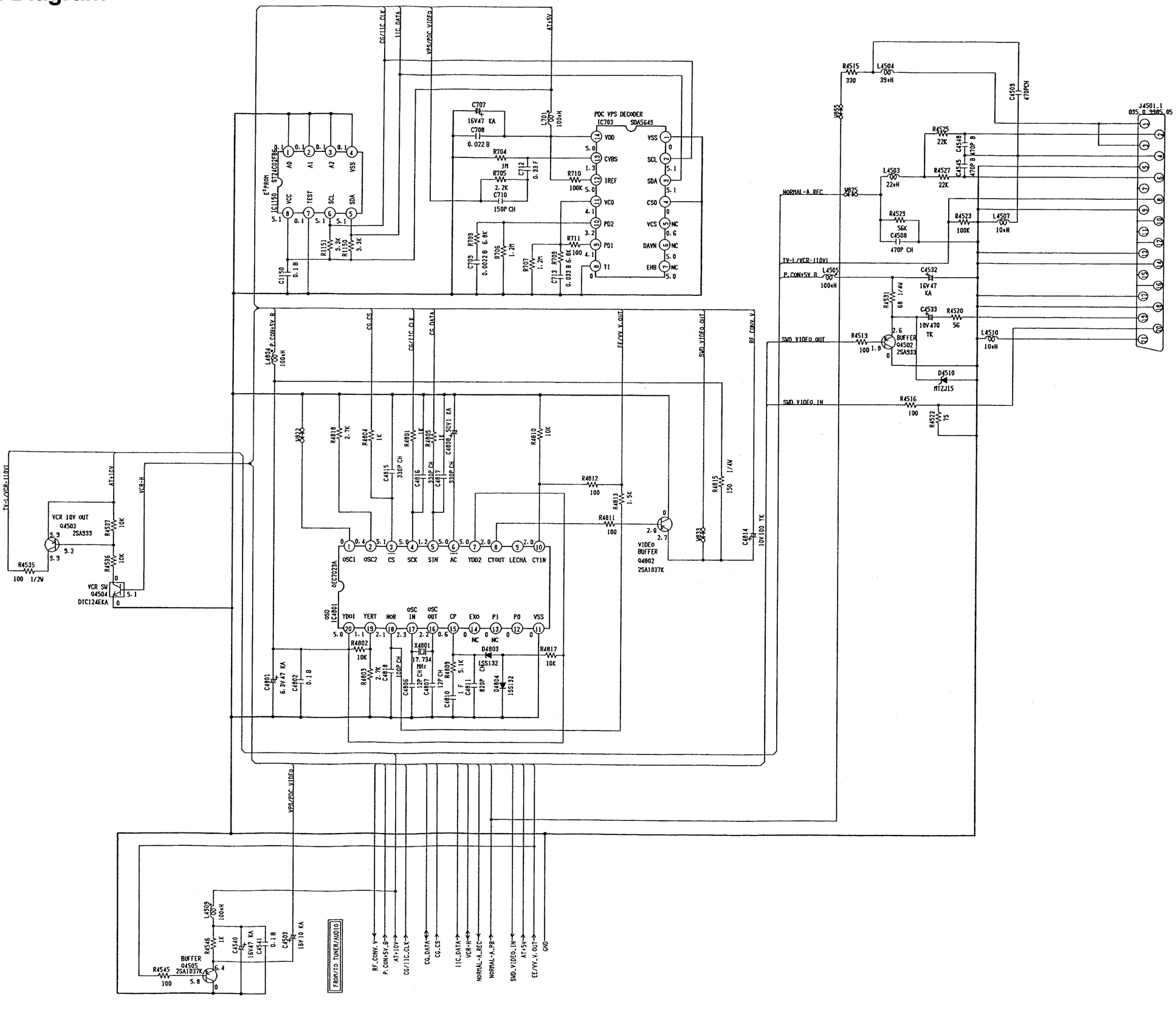
Operations Display Diagram



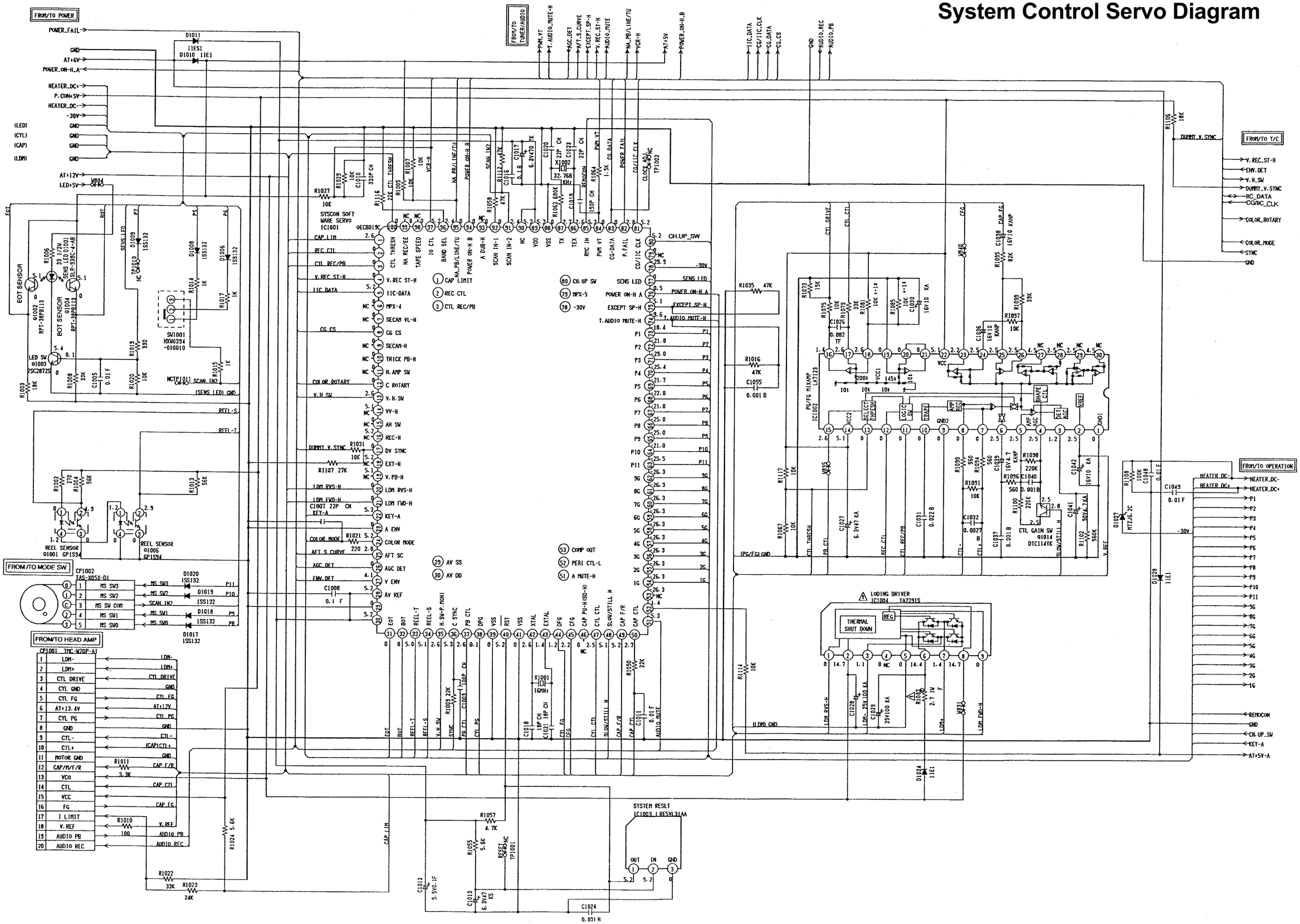
Power Supply Diagram



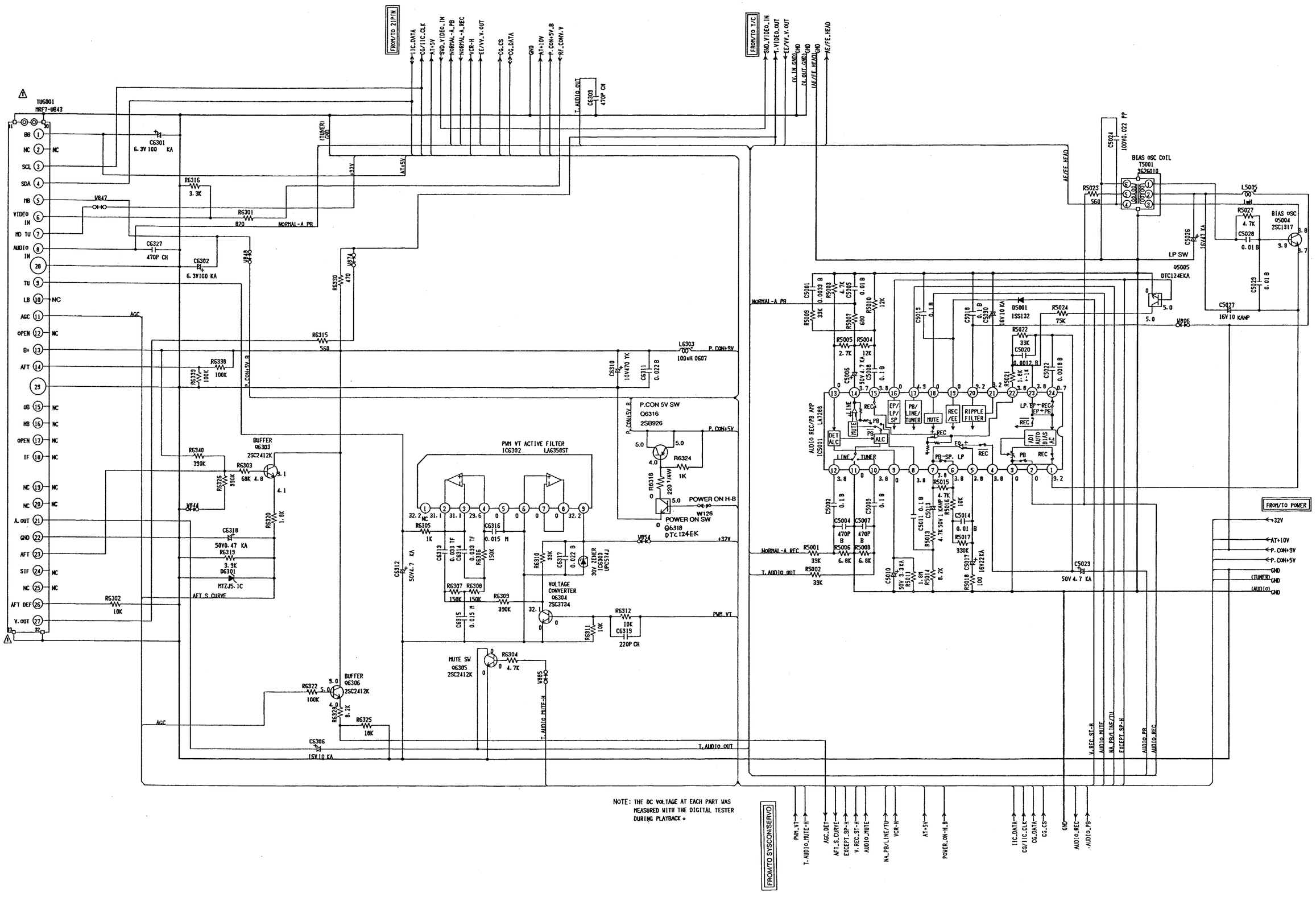
Scart AV Switch Diagram



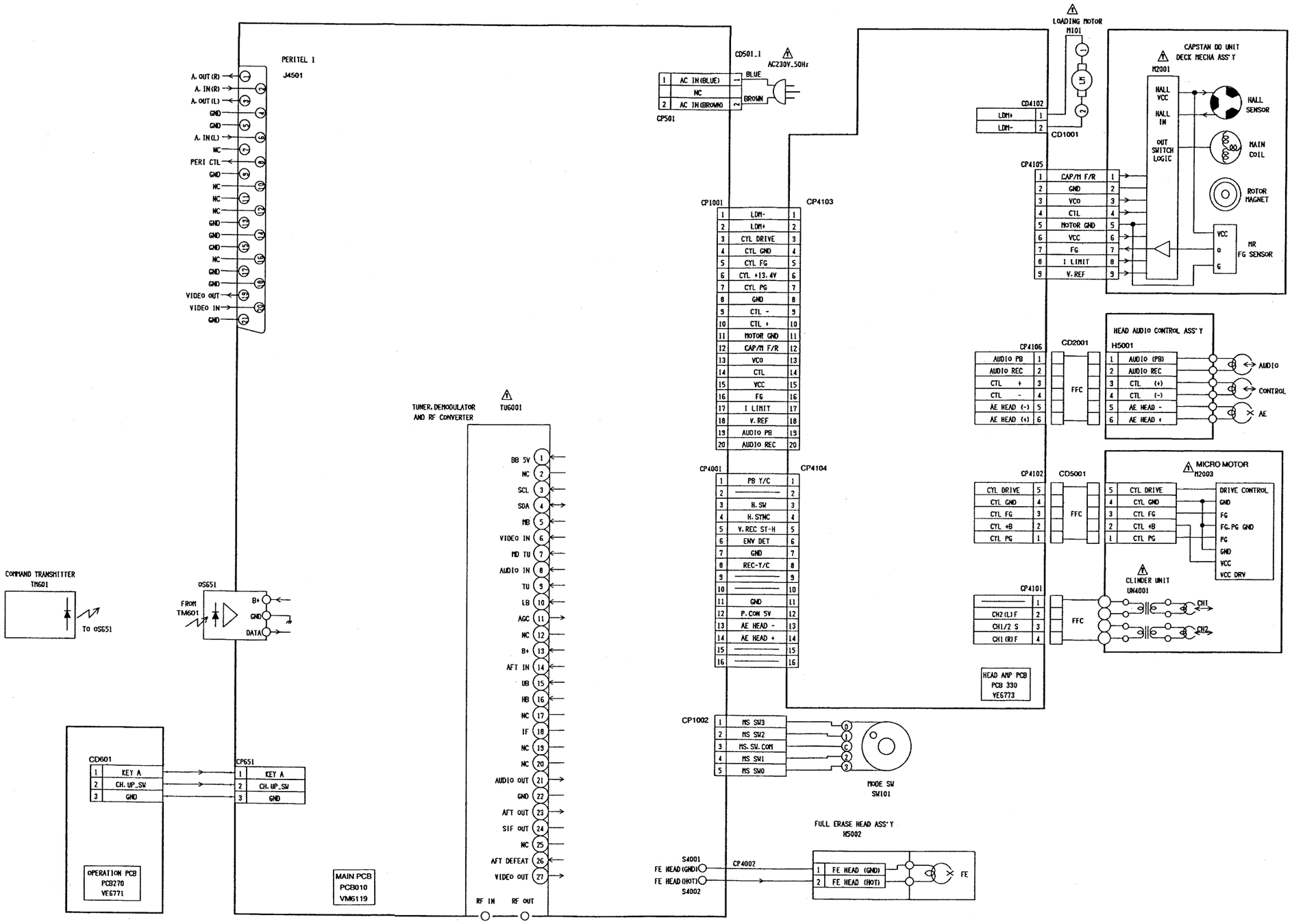
System Control Servo Diagram

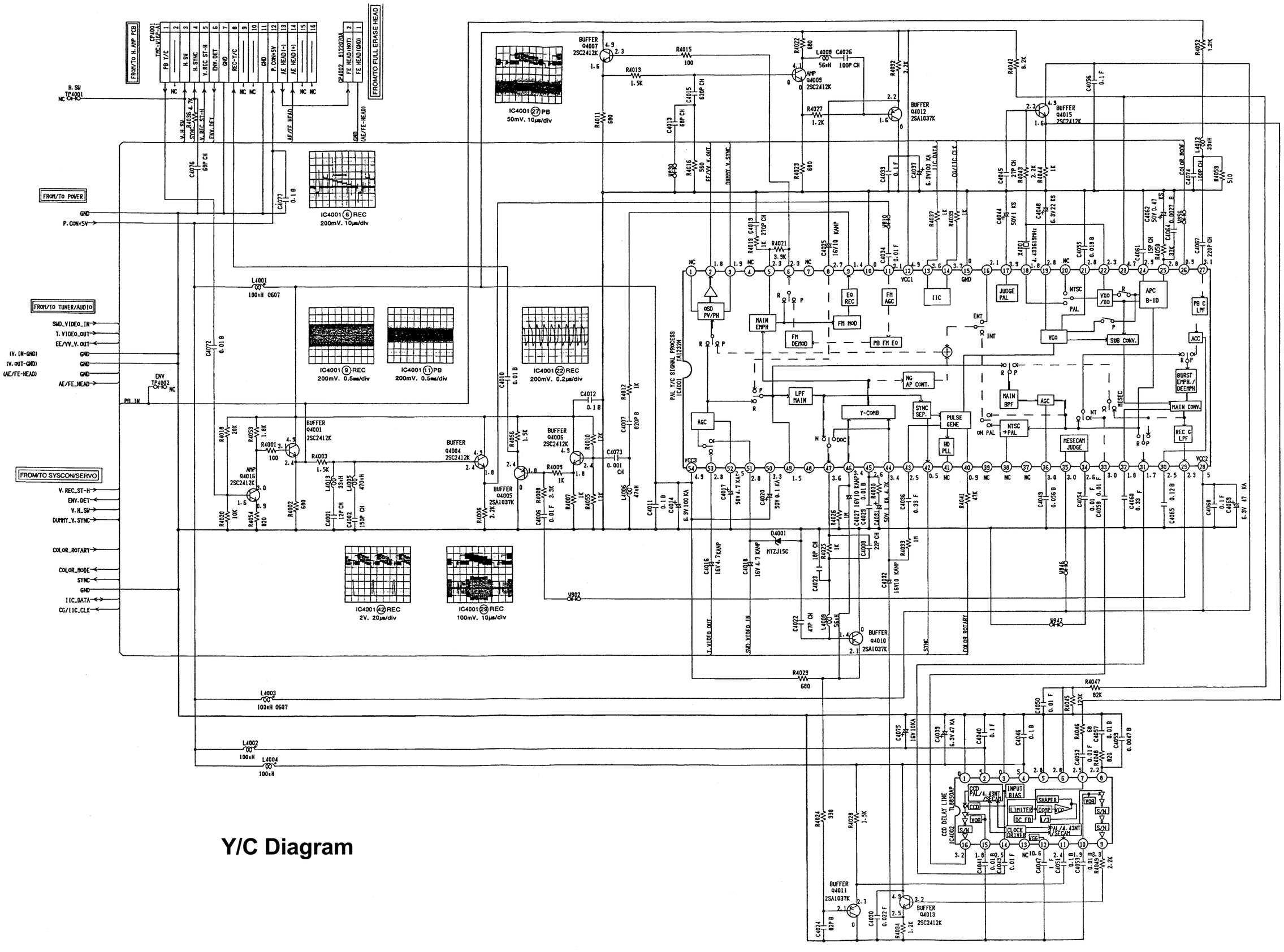


Tuner Audio Diagram



Wiring Diagram




FROM/TO H.AMP PCB
CP4001 TMC-V16Z-A1
PB Y/C
H. SW
H. SYNC
V. REC ST-H
ENV. DET
GND
REC-Y/C
GND
P. CON+5V
AE HEAD(-)
AE HEAD(+)
CP4002 B12203DA
FE HEAD(HOT)
FE HEAD(GND)
FROM/TO FULL ERASE HEAD
IC4001 (27) PB 50mV. 10μs/div
IC4001 (6) REC 200mV. 10μs/div
FROM/TO POWER
GND
P.CON+5V
FROM/TO TUNER/AUDIO
SWD_VIDEO_IN
T. VIDEO_OUT
EE/YV_V. OUT
(V. IN-GND)
(V. OUT-GND)
(AE/FE-HEAD)
AE/FE_HEAD
ENV TP4002
PB_IN
IC4001 (9) REC 200mV. 0.5ms/div
IC4001 (11) PB 200mV. 0.5ms/div
IC4001 (22) REC 200mV. 0.2μs/div
FROM/TO SYSCON/SERVO
V. REC_ST-H
ENV. DET
V. H_SW
DUMMY_V. SYNC
COLOR_ROTARY
COLOR_MODE
SYNC
GND
IIC_DATA
CG/IIC_CLK
IC4001 (42) REC 2V. 20μs/div
IC4001 (29) REC 100mV. 10μs/div
PAL Y/C SIGNAL PROCESS TA1232N IC4001
CCD DELAY LINE MN9504P IC4002
BUFFER Q4007 2SC2412K
BUFFER Q4009 2SC2412K
BUFFER Q4012 2SA1037K
BUFFER Q4015 2SC2412K
BUFFER Q4001 2SC2412K
BUFFER Q4004 2SC2412K
BUFFER Q4005 2SC2412K
BUFFER Q4005 2SA1037K
BUFFER Q4010 2SA1037K
BUFFER Q4011 2SA1037K
BUFFER Q4013 2SC2412K
AMP Q4016 2SC2412K
AMP Q4009 2SC2412K
D4001 MTZJ15C
L4001 100μH 0607
L4003 100μH 0607
L4002 100μH
L4004 100μH
X4001 4.433619MHz
OSD PV/PH
MAIN EMPH
FM DEMOD
FM MOD
PB FM EQ
EQ REC
FM AGC
IIC
JUDGE PAL
VCO
VXO /XO
APC B-ID
SUB CONV.
ACC
BURST EMPH./ DEEMPH
MAIN CONV.
REC C LPF
PB C LPF
NG AP CONT.
LPF MAIN
Y-COMB
SYNC SEP.
PULSE GENE
HD PLL
MAIN BPF
AGC
NTSC →PAL
MESECAM JUDGE
INPUT BIAS
SHAPER
LIMITER
COMP
DC FB
CLOCK DRIVER
VCC1
VCC2
VCC3
GND
INT
EXT
NTSC
PAL
MESECAM
VB01
VB02
VB03
VB04
VB05
VB06
VB07

Y/C Diagram